SEP 0 7 1005 W

0 7 2005 (1)		PTO/SB/21 (09-04)
Approved for use through 07/31/2006. OMB 0651-0031 U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE		
Uode The Paperwork Reduction Act of 1995	no persons are required to respond to a co Application Number	llection of information unless it displays a valid OMB control number. 10/698,169
TRANSMITTAL	Filing Date	October 31, 2003
FORM	First Named Inventor	Aronowitz, Sheldon
	Art Unit	2814
(to be used for all correspondence after initial	Examiner Name	Mai, Anh D.
Total Number of Pages in This Submission	27 Attorney Docket Number	02-6037
ENCLOSURES (Check all that apply)		
Fee Transmittal Form	✓ Drawing(s)	After Allowance Communication to TC
Fee Attached	Licensing-related Papers	Appeal Communication to Board of Appeals and Interferences
Amendment/Reply After Final Affidavits/declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statement Certified Copy of Priority Document(s) Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53	Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Change of Correspondence A Terminal Disclaimer Request for Refund CD, Number of CD(s) Landscape Table on CE Remarks Additional Enclosure:	Address Status Letter Other Enclosure(s) (please Identify below): 1. Power of Attorney and Correspondence Address Indication Form, PTO/SB/81 2. Statement Under 37 CFR 3.73(b), PTO/SB/96 3. Petrop Research
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm Name LSI Logic Corporation		
Signature LAGL		
Printed name Timothy R. Croll		
Date 6 Sept 0	5	Reg. No. 36,771
CERTIFICATE OF TRANSMISSION/MAILING I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:		
Signature Mal July		
Typed or printed name Mark Salvatore	,	Date 9-7-05

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sheldon Aronowitz Vladimir Zubkov

Grace Sun

Serial No.:

10/698,169

Filed:

October 31, 2003

For:

Memory Device Having an Electron

Trapping Layer in a High-K

Dielectric Gate Stack

)

Group Art Unit: 2814

Examiner:

Mai, Anh D.

Atty Docket:

LSI1P218 / 02-6037

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Mark Salvatore

ptember 7, 2005

Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 6 Sept 05

Respectfully submitted,

Timothy Croll

Reg. No. 36,771